



ST6624

Figure 7.6 Analog ASIC D2301

Table 7.8 Signals measured on analog ASIC D2301

Logic 0=0V Logic 1=+5V

Comp	Name Circ/IC	Scope	Freq.	Data H/L/A	Description
1	TRIG		0		EXT. Trigger input
2	GNDa		0		Ground Analog-input-circuit
3	INPB		10 kHz		Input signal B
4	VCCa		0		Positiv power supply Analog-input-circuit
5	CDAT				Serial Data line; HIGH pulses at AUTOSSET
6	CCLK				Clock line; LOW pulses at AUTOSSET
7	DTAE				Latch enable line; HIGH pulses at AUTOSSET
8	GNDt		0		Ground Trigger-output-circuit